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Intrepid: an SMT-based Model Checker for Control Engineering and Industrial Automation



## **Control Engineering**

#### **Control Engineering**



#### **Avionics & Automotive**

- Strict software development process, encoded in standards (e.g., DO-178C)
- Requirement-centric process
- Derive software from reqs.
- Provide tests to witness that requirements are met



#### **Avionics & Automotive**

## Google

boeing 737 max	
boeing 737 max 8	
boeing 737 max crashes	
boeing 737 max design flaws	
boeing 737 max news	
boeing 737 max grounding	
boeing 737 max explained	
boeing 737 max flight simulator	
boeing 737 max pilot training	

The Maneuvering Characteristics Augmentation System (MCAS), a newly introduced automated flight control on the MAX, was suspected of forcing both aircraft into a dive due to erroneous data from an angle of attack (AoA) sensor. The airplane flight manual contained no description of MCAS, and pilots had no knowledge of the system until the Lion Air crash. Aviation engineers criticized Boeing's safety analysis of MCAS: the system used only one of two AoA sensors, creating a single point of failure; it could move the tail beyond operating limits specified in the certification; and it could repeatedly override pilot control and push down the airplane nose. After the accidents Boeing disclosed that a cockpit indicator to warn of AoA sensor failure was inoperative. Boeing insisted the aircraft was safe, but admitted MCAS was a factor in both accidents.

Q Search

Bloomberg

#### Technology

**Boeing's 737 Max Software Outsourced to \$9-an-Hour Engineers** 





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- IEC-61131 defines 5 programming languages for PLCs
- Two textual (ST, IL)
- Two graphical (FBD, LD)
- A "mixed" (SFC)

```
END IF;
Setpoint_IN_STAGE_1_FAILED:
    (* During 'STAGE_1_FAILED': '<S1>:119' *)
   IF (stage3 sensor <= 0) OR (stage2 sensor <= 0) THEN
        (* Transition: '(<S1>:150' *)
        IF stage2 sensor > 0 THEN
            (* Transition: '<S1>:155' *)
            is _c2 _Setpoint := Setpoint _IN _STAGES _1 _3 _FAILED;
            (* Entry 'STAGES 1 3 FAILED': '<S1>:120' *)
            rtb stage1 setpoint := L0;
           rtb_stage2_setpoint := L0 - overall_target;
            distributed target := rtb stage2 setpoint;
        FLSE
            (* Transition: '<S1>:154' *)
            IF stage3 sensor > 0 THEN
                (* Transition: '<S1>:159' *)
                is c2 Setpoint := Setpoint IN STAGES 1 2 FAILED;
                (* Entry 'STAGES 1 2 FAILED': '<S1>:121' *)
                rtb stagel setpoint := L0;
                rtb stage2 setpoint := L0;
               distributed target := L0 - overall target;
            FLSE
               guard 0 := TRUE;
           END IF;
        END IF;
   ELSE
       guard _ 0 := TRUE;
    END IF;
```



Software Sabotage How Stuxnet disrupted Iran's uranium enrichment program

1 The malicious computer worm probably entered the computer system which is normally cut off from the outside world at the uranium enrichment facility in Natanz via a removable USB memory stick.

2 The virus is controlled from servers in Denmark and Malaysia with the help of two Internet addresses, both registered to false names. The virus Infects some 100,000 computers around the world.

Stuxnet spreads through the system until It finds computers running the Slemens control software Step 7. which is responsible for regulating the rotational speed of the centrifuges.

4 The computer worm varies the rotational speed of the centrifuges. This can destroy the centrifuges and Impair uranium enrichment.

5 The Stuxnet attacks start in June 2009. From this point on, the number 3,936 of inoperative centrifuges increases sharply.



Iranian centrifuges for uranium enrichment





#### **Control Engineering Languages**



### **Control Engineering Languages**

- Types
  - Booleans
  - Signed Integers (SINT, INT, ...)
  - Unsigned Integers (USINT, UINT, ...)
  - Floats (REAL, LREAL)
- Semantics of the above:
  - Fixed-width
  - Discrete evolution of memory values

# Intrepid intro

#### Intrepid's guiding principles

- Fast simulation

-Bit-precise

- Scriptable

#### - Parsing real-world languages

#### Intrepid: a model-checking library

#### -Backend: C++ engine (intrepid)

- State representation (SMT formulas in Z3)
- State exploration (Satisf. and QE calls to Z3)
- Exposes a C API

## - Python API (intrepyd)

- Wraps the C API, and provides OO Python API
- Retains efficiency, but provides flexibility and fun

#### Intrepid's input language

- There is no input language: you write benchmarks directly in Python

```
def mkFunc1(ctx, in1, in2):
1
                                                         from intrepyd import context
                                                     1
 2
       return ctx.mk_and(in1, in2)
                                                         import example1
                                                     2
 3
                                                     3
     def mkCircuit(ctx):
 4
                                                        def main():
                                                    4
 5
       boolType = ctx.mk_boolean_type()
                                                     5
                                                           ctx = context.Context()
       in1 = ctx.mk_input('in1', boolType)
 6
                                                    6
                                                           myCirc = example1.mkCircuit(ctx)
       in2 = ctx.mk_input('in2', boolType)
 7
                                                           # do other interesting stuff here
                                                     7
 8
       in3 = ctx.mk input('in3', boolType)
                                                    8
       myCirc1 = mkFunc1(ctx, in1, in2)
 9
                                                    9
                                                         if __name__ == "__main__":
       myCirc2 = mkFunc1(ctx, in1, in3)
10
                                                   10
                                                           main()
11
       myCirc3 = mkFunc1(ctx, myCirc1, myCirc2)
```

## Intrepid's input language

## Some advantages

- Functions and classes x = ctx.
- Benchmarks are prograinputs = 🖓 mk\_div inputs = [ctx.mk\_input('in' -
- Can natively import the
- Autocompletion

x = ctx. boolType inputs = ③ mk\_boolean\_type ③ mk\_div ③ mk\_eq ③ mk\_false ③ mk\_float16\_type ③ mk\_float32\_type ③ mk\_float64\_type ③ mk\_geq ④ mk\_gt ⑤ mk\_iff

from intrepyd import context

- I don't have to maintain a parser



#### **Intrepid's Simulator**

- Linear-time in size of circuit
- Fills out values of a "trace" object
  - Values for inputs can be specified for specific timestamps, otherwise they are defaulted to false/0
- Traces can be converted into pandas dataframes
- Counter-examples are traces, so they can be readily resimulated to check their validity

## **Intrepid's Engines**

#### - BMC

- Finds counterexamples for some targets, at some depth

#### - Optimizing BMC

- Find counterexamples that satisfies the highest number of targets

#### - Backward Reachability

- Finds counterexamples and proves targets unreachable

## **Intrepid's Engines**

- Multi-target engines
- Target: a Boolean signal that we want to reach
- Watch: values that we want to see in trace

26		cl
27		
28		
29		
30		
31	+	
34		
35	+	
40		
41	+	
47		
48	+	
56		
57	+	
62		
63	+	
68		
69	+	
74		
75	+	
80		
81	+	

```
.ass Engine(object):
  .....
  Abstract intreface for an Engine
  111111
  def __init__(self, ctx):...
  def add_target(self, net):...
  def reach_targets(self): ...
  def get_last_reached_targets(self): --
  def get_last_trace(self): ...
  def remove_last_reached_targets(self): ...
  def add_watch(self, net):...
  def can prove(self): ...
  def can optimize(self):...
```

#### **Intrepid's Engines**



## An example application: ATG

- MC/DC is a testing criterion defined in DO-178C, for critical software
- Decision: a sub-circuit with a Boolean output
- Condition: a Boolean net in the decision that needs to be observed
- Task: given a decision D, for each condition C find two tests T1, T2 such that
  - C has value true in T1
  - C has value false in T2
  - D evaluates differently in T1 and T2

- Each row is a test
- Tests 0 and 1 show MC/DC for A

	Α	В	С	Ο	
0	Т	Т	F	Т	
1	F	Т	F	F	
2	Т	F	F	F	
3	F	F	Т	Т	
4	F	F	F	F	



- To come up with suitable tests (the table) is easy
- ... but, the less tests are produced, the better
  - Tests are to be written down on tables and reviewed by the FAA (no kidding)
- It is not so easy, it is an optimization problem
- Also, not merely combinational, sequential part plays a role too
- Need for an optimizing-BMC



# Parsers for real-world industrial languages

#### **Control Engineering Languages**



#### **Lustre to Python**

- Parser written in Python using ANTLR

Takes Lustre, dumps Intrepyd's Python API
 benchmark.lus => benchmark.py

- Good collection of benchmarks (Kind2), thanks for the effort of collecting them

#### Simulink/Stateflow to Python

- Simulink to Python: initial translation implemented on top of ConQAT Java libraries
  - Very fast but
  - A pain to implement in detail and to maintain
  - -Need to infer data types
- State low to Python: a real nightmare
  - No available specification of the language!
  - Need to guess behavior via simulation

#### Simulink/Stateflow to IEC-61131 ST to Python

- Matlab provides a toolkit called Simulink PLC Coder that generates IEC-61131 ST
- Two birds with one stone:
  - We can indirectly handle Simulink/Stateflow
  - We can set foot in the Industrial Automation world
- No need to parse the "whole" ST language, but only a subset (i.e., no loops)
- Parser implemented again with ANTLR in Python

#### Simulink/Stateflow to IEC-61131 ST





## Experiments

#### **Intrepid vs Luke on Invalid benchmarks**

- Basically two different implementation of BMC
- Solved by Intrepid: 341 in 589 s
- Solved by Luke: 342 in 3219 s
- <u>https://plot.ly/create/?fid=robertobrutt</u> omesso:30#/

Intrepyd vs Luke on known Invalid kind2 benchmarks



#### **Intrepid vs Luke on Valid benchmarks**

- Basically TI vs Backward Reach
- Solved by Intrepid: 182 in 3242 s
- Solved by Luke: 137 in 335 s
- <u>https://plot.ly/create/?fid=robertobrutt</u> omesso:32#/

Intrepyd vs Luke on known Valid kind2 benchmarks



intrepyd

#### **Intrepid vs Luke on Valid benchmarks**

- Solved by Intrepid overall:
   523 in 3831 s
- Solved by Luke overall: 479 in 3557 s
- <u>https://plot.ly/create/?fid=r</u> <u>obertobruttomesso:36#/</u>

Intrepyd vs Luke on kind2 benchmarks



#### **Preliminary experiments: GPCA Simulink/Stateflow**

- Benchmark from the CocoSim suite (<u>https://coco-team.github.io/cocosim/</u>)
- Simulink/Stateflow model of an infusion pump
- Translated into IEC-61131 ST with Matlab and then into Python with our frontend (takes a few seconds)
- Out of 8 properties, 4 can be solved in about 50 seconds (14 seconds for parsing)

# Conclusion

#### How to get intrepid

- Intrepid is open-source, BSD-3 licensed
- Works on Windows and Linux "officially"
- repo = <u>https://github.com/formalmethods</u>
- Backend: repo/intrepid
- Python API: repo/intrepyd
- -pip install intrepyd
- Blog: https://formalmethods.github.io

![](_page_39_Picture_0.jpeg)

#### **Thank You**

www.nozominetworks.com